

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10728492	
	Filing Date		2003-12-05	
	First Named Inventor	Eric Hung		
	Art Unit	2115		
	Examiner Name	Ji H. Bae		
	Attorney Docket Number	03-1237 5201-27600		

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1	JEDEC Standard, "Double Data Rate (DDR) SDRAM Specification, May 2005.	<input type="checkbox"/>
2	Circuits, Interconnections, and Packaging for VLSI by BAKOGLU, 1990, p. 321.	<input type="checkbox"/>

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¹ See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached.

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Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

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- ☐ See attached certification statement.
- ☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Kevin L. Daffer/	Date (YYYY-MM-DD)	2007-05-21
Name/Print	Kevin L. Daffer	Registration Number	34146

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